



Schematic diagram

Marking and pin assignment

SOT-23 top view

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE2302 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

- V_{DS} = 20V,I_D =4A
 - $R_{\text{DS(ON)}} < 59 \text{m}\Omega @ V_{\text{GS}} = 2.5 \text{V}$
 - $R_{DS(ON)} < 45m\Omega @ V_{GS}=4.5V$
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Battery protection
- Load switch
- Power management

Package Marking and Ordering Information

Device Marking	Device	Dev	ice Package	Reel Size	Tape width	Quantity
2302 X	NCE2302		SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	Vds	20	V	
Gate-Source Voltage	Vgs	±12	V	
Drain Current-Continuous	ID	4	А	
Drain Current-Pulsed (Note 1)	I _{DM}	10	А	
Maximum Power Dissipation	PD	1	W	
Operating Junction and Storage Temperature Range	T_{J},T_{STG}	-55 To 150	°C	

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{θJA}	125	°C /W
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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	20	22	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =20V, V_{GS} =0V	-	-	1	μA





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Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)		•	·	1		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	0.5	0.85	1.2	V
Desir Devene Or Otata Desistance	_	V _{GS} =2.5V, I _D =2.5A	-	37	59	mΩ
Drain-Source On-State Resistance	$R_{DS(ON)}$	V _{GS} =4.5V, I _D =2.9A	-	30	45	mΩ
Forward Transconductance	g fs	V _{DS} =5V,I _D =2.9A	-	8	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	Clss		-	300	-	PF
Output Capacitance	Coss	V_{DS} =10V, V_{GS} =0V,	-	120	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	A	80	-	PF
Switching Characteristics (Note 4)				1		
Turn-on Delay Time	t _{d(on)}		-	10	15	nS
Turn-on Rise Time	tr	V _{DD} =10V,I _D =2.9A	-	50	85	nS
Turn-Off Delay Time	$t_{d(off)}$	V _{GS} =4.5V R _{GEN} =6Ω	-	17	45	nS
Turn-Off Fall Time	t _f	15	-	10	20	nS
Total Gate Charge	Qg	V _{DS} =10V,I _D =2.9A, V _{GS} =4.5V	-	4.0	10	nC
Gate-Source Charge	Q _{gs}		-	0.65	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =4.5V	-	1.2	-	nC
Drain-Source Diode Characteristics	10	;	•			•
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =2.9A	-	0.75	1.2	V
Diode Forward Current (Note 2)	Is		-	-	4	Α
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Notes:

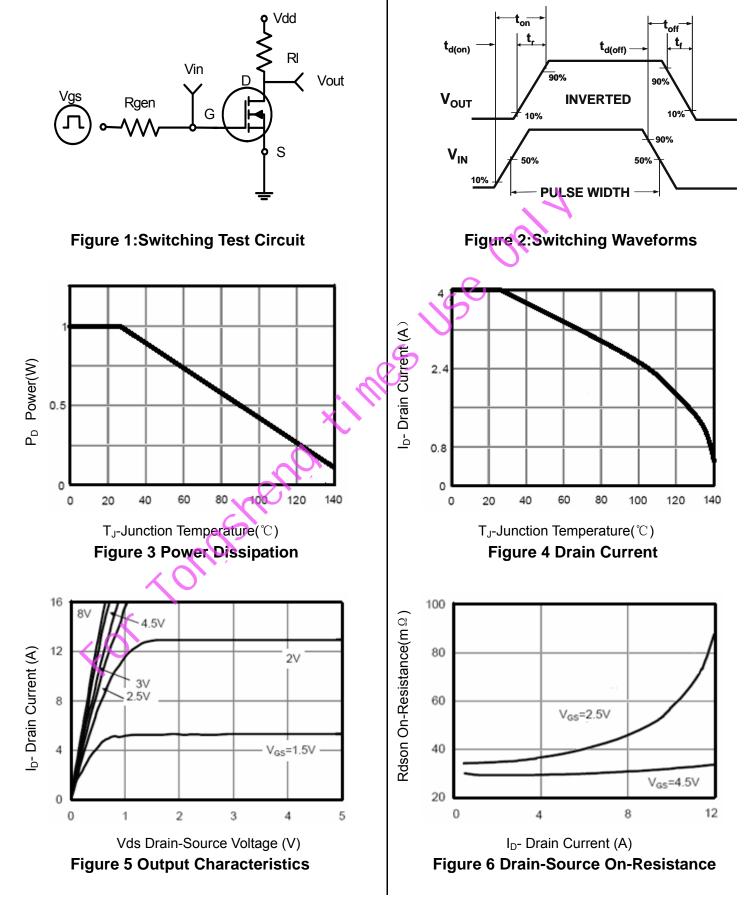
- **1.** Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production





NCE2302

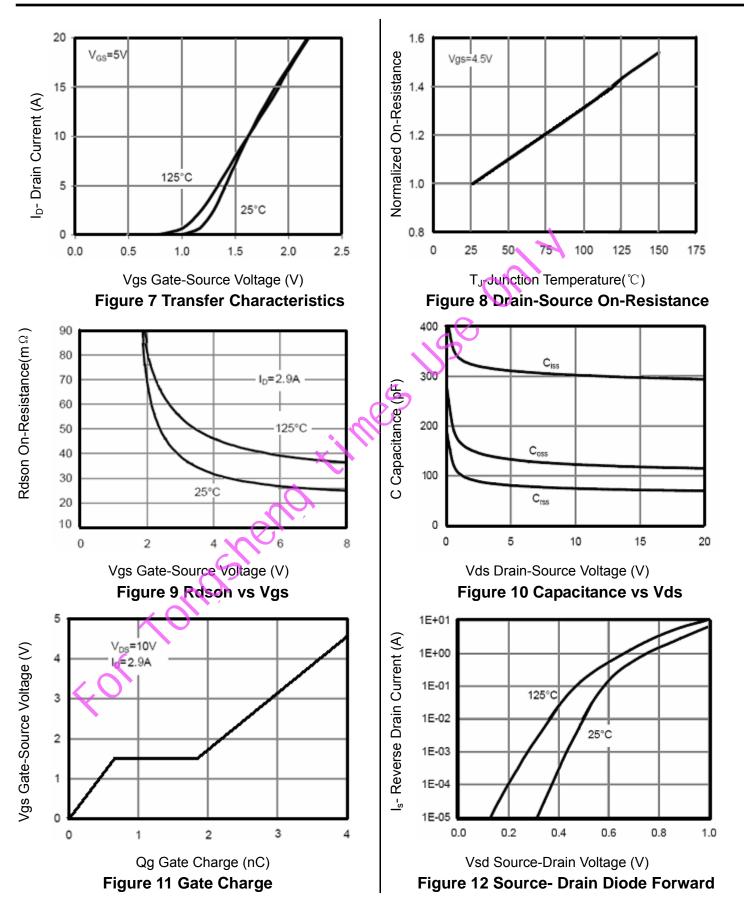
Typical Electrical and Thermal Characteristics





Pb Free Product

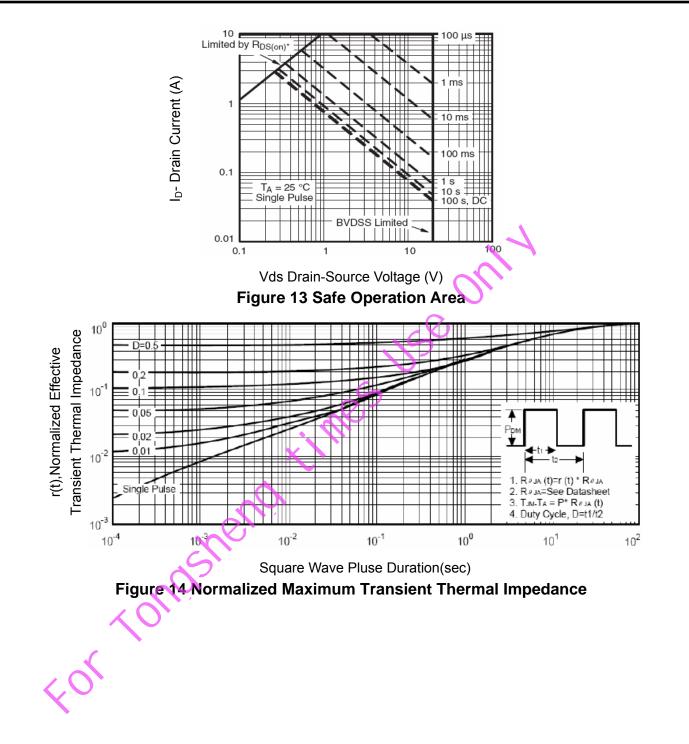
NCE2302



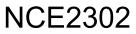
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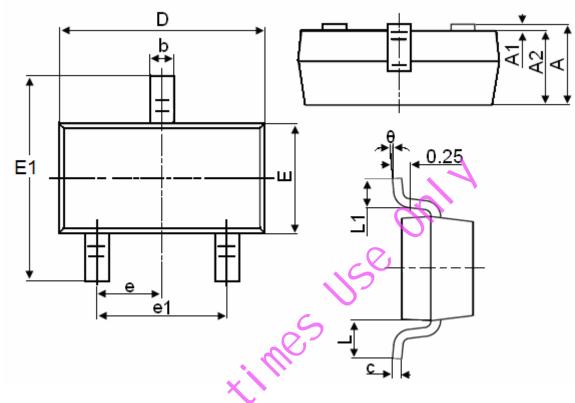








SOT-23 Package Information



Symbol	^	Dimensions in Millimeters	
Symbol	MIN.	MAX.	
А	0.900	1.150	
A1	0.000	0.100	
A2	0.900	1.050	
b	0.300	0.500	
с	0.080	0.150	
D	2.800	3.000	
H.	1.200	1.400	
E1	2.250	2.550	
е	0.950TYP		
e1	1.800	2.000	
L	0.550REF		
L1	0.300	0.500	
θ	0°	8°	

Notes

1. All dimensions are in millimeters.

2. Tolerance ± 0.10 mm (4 mil) unless otherwise specified

3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.







4. Dimension L is measured in gauge plane.

5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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