

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE3095G uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

100% UIS TESTED! 100% ΔVds TESTED!

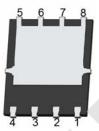
General Features

- $V_{DS} = 30V, I_{D} = 95A$
 - $R_{DS(ON)}$ =4.1m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =5.1m Ω (typical) @ V_{GS} =4.5V
- High density cell design for ultra low Rdson
- Very low on-resistance R_{DS(on)}
- Good stability and uniformity with high E_{AS}
- 150 °C operating temperature
- Pb-free lead plating

DFN 5X6







S2 S3 G4 F5D

Top View

Bottom View

Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE3095G	NCE3095G	DFN 5x6-8L	-	-	-

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	95	Α
Drain Current-Continuous(T _C =100 ℃)	I _D (100℃)	63.6	Α
Pulsed Drain Current	I _{DM}	300	Α
Maximum Power Dissipation	P _D	80	W
Derating factor		0.64	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	150	mJ
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{ heta JC}$	1.56	°C/W



Electrical Characteristics (TC=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•	•		
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	30	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	_	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			•	•		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	1	1.5	2.5	V
Drain-Source On-State Resistance	-	V _{GS} =10V, I _D =20A	-	4.1	5.1	- mΩ
Diani-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =20A	-	5.5	8.5	
Forward Transconductance	g FS	V _{DS} =5V,I _D =20A	30	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ -45\/\/ -0\/	-	1784	-	PF
Output Capacitance	C _{oss}	V _{DS} =15V,V _{GS} =0V,		266	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	212	_	PF
Switching Characteristics (Note 4)	·					
Turn-on Delay Time	t _{d(on)}		-	7	-	nS
Turn-on Rise Time	t _r	$V_{DD}=5V,I_{D}=20A$	-	6	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{GEN} =6 Ω	-	30	-	nS
Turn-Off Fall Time	t _f		-	8	-	nS
Total Gate Charge	Qg	V -45VI -20A	-	38.4	-	nC
Gate-Source Charge	Q _{gs}	$V_{DS}=15V,I_{D}=20A,$ $V_{GS}=10V$	-	5.8	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	7.9	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-	0.85	1.2	V
Diode Forward Current (Note 2)	Is		-	-	95	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, I _F = 20A	-	-	47	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	-	25	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

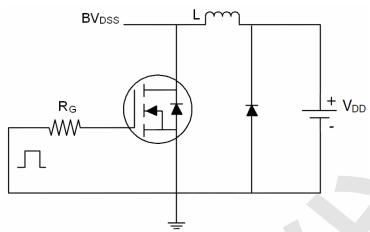
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 2\%$.
- 4. Guaranteed by design, not subject to production

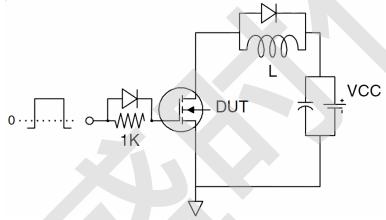


Test Circuit

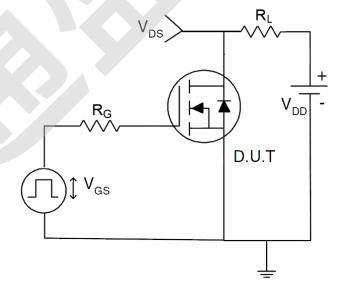
1) E_{AS} Test Circuits



2) Gate Charge Test Circuit

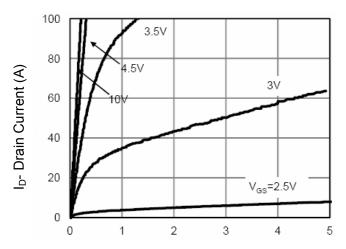


3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics

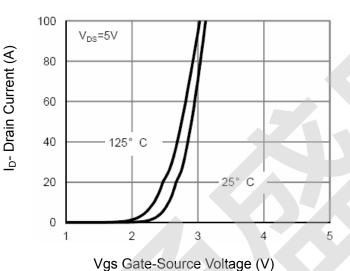


Figure 2 Transfer Characteristics

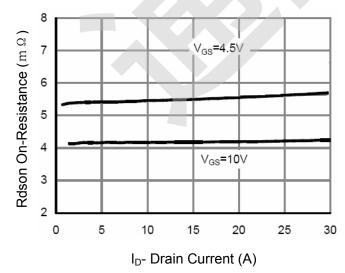


Figure 3 Rdson- Drain Current

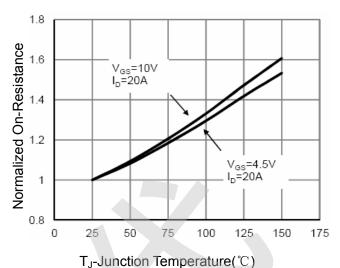


Figure 4 Rdson-Junction Temperature

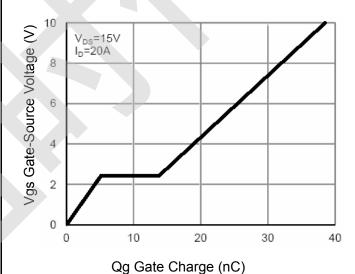


Figure 5 Gate Charge

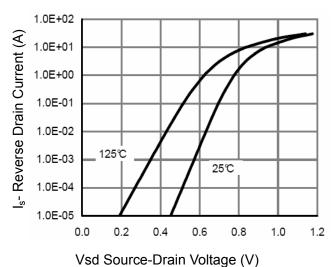


Figure 6 Source- Drain Diode Forward



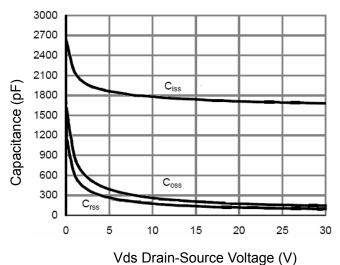
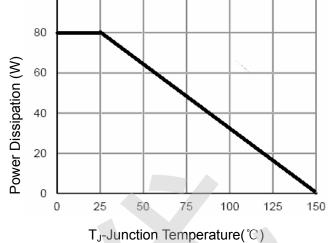


Figure 7 Capacitance vs Vds



100

Figure 9 Power De-rating

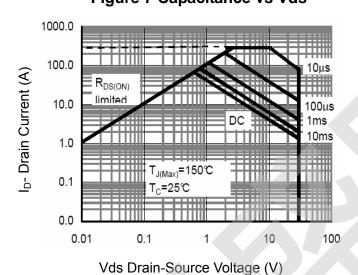
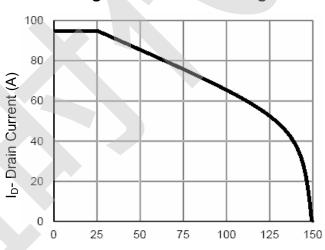


Figure 8 Safe Operation Area



 T_J -Junction Temperature($^{\circ}$ C) Figure 10 Current De-rating

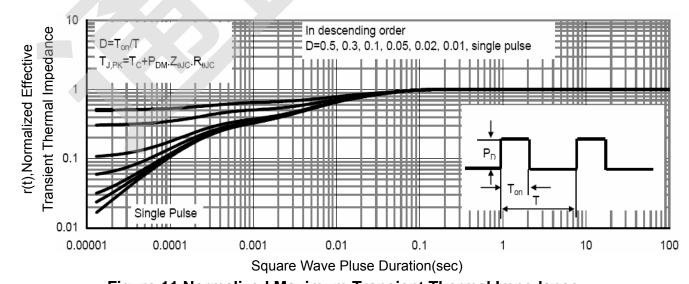
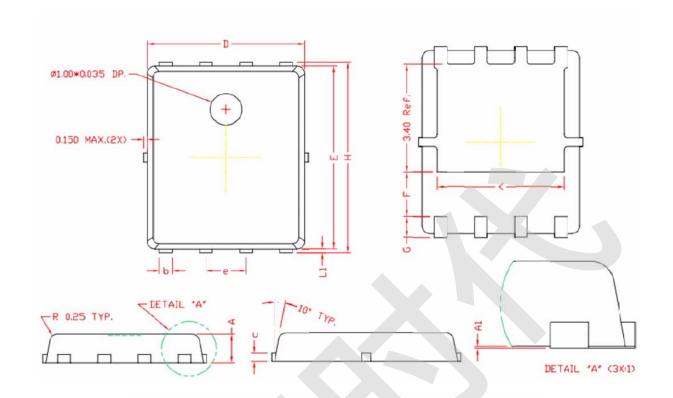


Figure 11 Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX	
A	0.80	0.90	1.00	
A1	0.00	0.03	0.05	
b	0.35	0.42	0.49	
c	0. 254 REF.			
D	4.90	5.00	5. 10	
F	1.40 REF.			
E	5.70	5.80	5. 90	
е	1. 27 BSC.			
Н	5. 95	6.08	6. 20	
L1	0.10	0. 14	0. 18	
G	0.60 REF.			
K	4.00 REF.			



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