

## **500V N-Channel Enhancement Mode MOSFET**

#### **Description**

The AP5N50BD is silicon N-channel Enhanced

VDMOSFETs, is obtained by the self-aligned planar Technology
which reduce the conduction loss, improve switching
performance and enhance the avalanche energy. The transistor
can be used in various power switching circuit for system
miniaturization and higher efficiency.



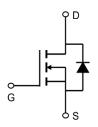
 $V_{DS} = 500V I_{D} = 5A$ 

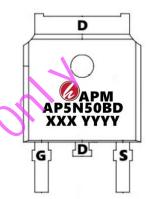
 $R_{DS(ON)} < 3.0\Omega$  @  $V_{GS}=10V$  (Type: 2.4 $\Omega$ )

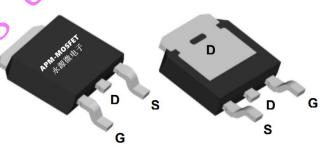
### **Application**

Uninterruptible Power Supply(UPS)

Power Factor Correction (PFC)







Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP5N50BD	TO-252-3L	AP5N50BD XXX YYYY	2500

### Absolute Maximum Ratings (T<sub>c</sub>=25°Cunless otherwise noted)

Symbol	Parameter	Value	Unit
VDSS	Drain-Source Voltage (V <sub>GS</sub> = 0V)	500	V
ID	Continuous Drain Current	5	Α
IDM	Pulsed Drain Current (note1)	15	Α
VGS	Gate-Source Voltage	±30	V
E <sub>AS</sub>	Single Pulse Avalanche Energy (note2)	57	mJ
IAR	Avalanche Current (note1)	2.4	Α
Ear	Repetitive Avalanche Energy note1)	6.4	mJ
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> = 25°C)	32.9	W
TJ, Tstg	Operating Junction and Storage Temperature Range	-55~+150	°C
RthJC	Thermal Resistance, Junction-to-Case	6.25	°C/W
RthJA	Thermal Resistance, Junction-to-Ambient	62.5	°C/W



### **500V N-Channel Enhancement Mode MOSFET**

### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

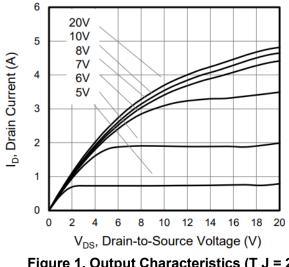
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_{D} = 250\mu A$	500	550		V
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 650V, V <sub>GS</sub> = 0V, T <sub>J</sub> =25°C			1	μA
IGSS	Gate-Source Leakage	V <sub>GS</sub> = ±30V			±100	nA
VGS(th)	Gate-Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.0	3.0	4.0	V
RDS(on)	Drain-Source On-Resistance (Note3)	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3.5A		2.4	3.0	Ω
C <sub>iss</sub>	Input Capacitance		4	310		
Coss	Output Capacitance	$V_{GS} = 0V$ , $V_{DS} = 25V$ , $f = 1.0MHz$		39		pF
Crss	Reverse Transfer Capacitance	$O_{I}$ .		6		
$Q_g$	Total Gate Charge	0		8		
Q <sub>gs</sub>	Gate-Source Charge	$V_{DD}$ =400V, $I_D$ = 3A, $V_{GS}$ = 10V		1.2		nC
Q <sub>gd</sub>	Gate-Drain Charge			5		
td(on)	Turn-on Delay Time	5		7.8		
t <sub>r</sub>	Turn-on Rise Time	$V_{DD}$ =250V, $I_D$ = 3A, $R_G$ = 25 $\Omega$		33		
td(off)	Turn-off Delay Time	VDD=250V, ID = 3A, RG = 2512		23		ns
t <sub>f</sub>	Turn-off Fall Time			59		
IS	Continuous Body Diode Current	T <sub>C</sub> = 25 °C			3.0	Α
ISM	Pulsed Diode Forward Current	10-20 0			12	Α
V <sub>SD</sub>	Body Diode Voltage	$T_J = 25^{\circ}C$ , $I_{SD} = 3A$ , $V_{GS} = 0V$			1.4	٧
trr	Reverse Recovery Time	V <sub>GS</sub> = 0V,I <sub>S</sub> = 3A, di <sub>F</sub> /dt =100A		80		ns
Qrr	Reverse Recovery Charge	/µs		1.8		μC

#### Note:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2. The EAS data shows Max. rating . IAS = 2.4A, VDD = 50V, RG = 25  $\Omega$ , Starting TJ = 25  $^{\circ}$ C
- 3、The lest condition is Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%
- 4. The power dissipation is limited by 150 ℃ junction temperature
- 5. The data is theoretically the same as ID and IDM, in real applications, should be limited by total power dissipation.

### **500V N-Channel Enhancement Mode MOSFET**

### **Typical Characteristics**



10<sup>1</sup> 10<sup>0</sup>  $T_{J} = 150^{\circ}C$ 10-1  $T_J = 25^{\circ}C$ 10-2 10-3 0.2 0.6 8.0 1.2 V<sub>SD</sub>, Source-to-Drain Voltage (V)

Is, Source Current (A)

Figure 1. Output Characteristics (T J = 25°C)

P<sub>D</sub>, Power Dissipation (w) D. Drain Current (A) 4 3 2 1

Figure 2. Body Diode Forward Voltage

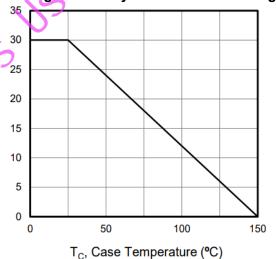


Figure 3 Drain Current vs. Temperature

T<sub>C</sub>, Case Temperature (°C)

90

150

120

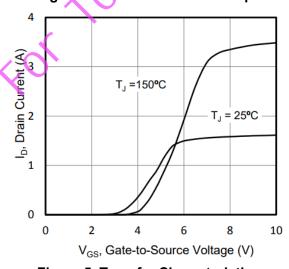


Figure 4. BV DSS Variation vs. Temperature

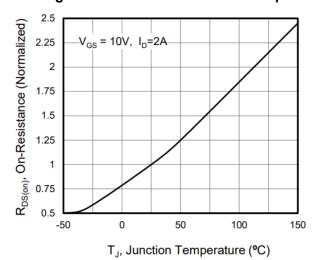


Figure 5. Transfer Characteristics

Figure 6. On-Resistance vs. Temperature

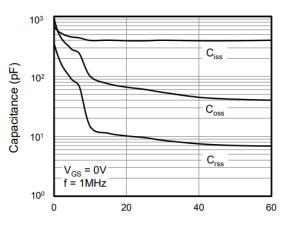
0

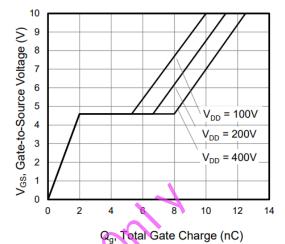
0

30



## **500V N-Channel Enhancement Mode MOSFET**





V<sub>DS</sub>, Drain-to-Source Voltage (V)

Figure 8. Gate Charge



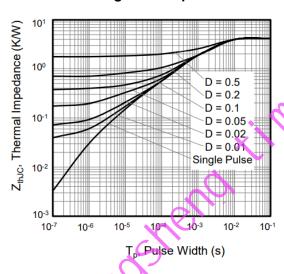
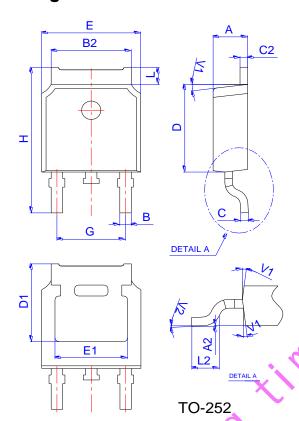


Figure 9. Transient Thermal Impedance



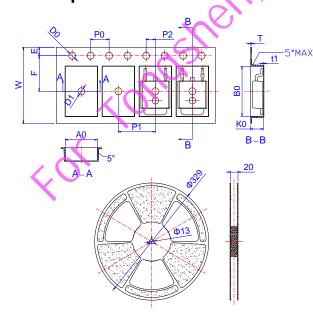
## **500V N-Channel Enhancement Mode MOSFET**

## **Package Mechanical Data**



Dimensions					
Millimeters			Inches		
Min.	Тур.	Max.	Min.	Тур.	Max.
2.10		2.50	0.083		0.098
0		0.10	0		0.004
0.66		0.86	0.026		0.034
5.18		5.48	0.202		0.216
0.40		0.60	0.016		0.024
0.44		0.58	0.017		0.023
5.90		6.30	0.232		0.248
5.30REF		0.209REF			
6.40		6.80	0.252		0.268
4.63	>		0.182		
4.47	)	4.67	0.176		0.184
9.50		10.70	0.374		0.421
1.09		1.21	0.043		0.048
1.35		1.65	0.053		0.065
	7°			7°	
0°		6°	0°		6°
	Min. 2.10 0 0.66 5.18 0.40 0.44 5.90 6.40 4.63 4.47 9.50 1.09 1.35	Min. Typ. 2.10 0 0.66 5.18 0.40 0.44 5.90 5.30REF 6.40 4.63 4.47 9.50 1.09 1.35	Millimeters           Min.         Typ.         Max.           2.10         2.50           0         0.10           0.66         0.86           5.18         5.48           0.40         0.60           0.44         0.58           5.90         6.30           5.30REF           6.40         6.80           4.63         4.67           9.50         10.70           1.09         1.21           1.35         1.65	Millimeters           Min.         Typ.         Max.         Min.           2.10         2.50         0.083           0         0.10         0           0.66         0.86         0.026           5.18         5.48         0.202           0.40         0.60         0.016           0.44         0.58         0.017           5.90         6.30         0.232           5.30REF         0         0.182           4.63         0.182         0.176           9.50         10.70         0.374           1.09         1.21         0.043           1.35         7°         0.053	Millimeters         Inches           Min.         Typ.         Max.         Min.         Typ.           2.10         2.50         0.083         0.00           0         0.10         0         0.026           5.18         5.48         0.202         0.016           0.40         0.60         0.016         0.044           5.90         6.30         0.232           5.30REF         0.209REF           6.40         6.80         0.252           4.63         0.182           4.47         4.67         0.176           9.50         10.70         0.374           1.09         1.21         0.043           1.35         7°         7°

# Reel Spectification-TO-252



	Dimensions					
Ref.	tef. Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
E	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
В0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
Т	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583



### 500V N-Channel Enhancement Mode MOSFET

#### **Attention**

- 1,Any and all APM Microelectronics products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your APM Microelectronics representative nearest you before using any APM Microelectronics products described or contained herein in such applications.
- 2,APM Microelectronics assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all APM Microelectronics products described or contained herein.
- 3, Specifications of any and all APM Microelectronics products described or contained here instipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- 4, APM Microelectronics Semiconductor CO., LTD strives to supply high quality high reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. Whendesigning equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- 5,In the event that any or all APM Microelectronics products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- 6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of APM Microelectronics Semiconductor CO., LTD.
- 7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. APM Microelectronics believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- 8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "DeliverySpecification" for the APM Microelectronics product that you Intend to use.





### **500V N-Channel Enhancement Mode MOSFET**

Edition	Date	Change
Rve1.0	2021/4/31	Initial release

Copyright Attribution"APM-Microelectronice"

For Tongsheng times Use Only