

## **40V N+P-Channel Enhancement Mode MOSFET**

#### Description

The AP25G04GD uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = 40V I<sub>D</sub> =28 A

 $R_{\text{DS(ON)}} < 24 \text{m}\Omega \ @ \ V_{\text{GS}} = 10 \text{V} \quad (\text{Type: } 18 \text{m}\Omega)$ 

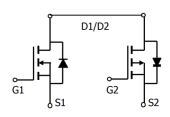
 $V_{DS} = -40V I_{D} = 25A$ 

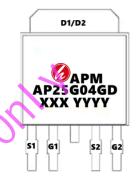
 $R_{DS(ON)} < 40m\Omega$  @  $V_{GS}=10V$  (Type:  $32m\Omega$ )

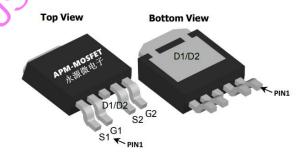
#### **Application**

Boost driver

Brushless motor







Package Marking and Ordering Information

Product ID		Pack	Marking	Qty(PCS)
AP25G04GD	, C	TO-252-4L	AP25G04GD XXX YYYY	2500

### Absolute Maximum Ratings (T<sub>c</sub>=25<sup>°</sup>Cunless otherwise noted)

0		Ra	Units	
Symbol	Parameter	N-Ch	N-Ch P-Ch	
VDS	Drain-Source Voltage	40	-40	V
Ves Gate-Source Voltage		±20	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	28	-25	А
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	18	-16	А
IDM Pulsed Drain Current <sup>2</sup>		46	-40	А
EAS	EAS Single Pulse Avalanche Energy³		66	mJ
IAS	Avalanche Current	17.8	-27.2	А
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	25	31.3	W
Тѕтс	Storage Temperature Range	-55 to 150	-55 to 150	°C
T <sub>J</sub> Operating Junction Temperature Range		-55 to 150	-55 to 150	°C
R <sub>0</sub> JA	R <sub>θJA</sub> Thermal Resistance Junction-Ambient <sup>1</sup>		62	
Rejc	Thermal Resistance Junction-Case <sup>1</sup>	5		°C/W



## **40V N+P-Channel Enhancement Mode MOSFET**

### Electrical Characteristics (T<sub>C</sub>=25°Cunless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	40	44		V	
△BVDSS/△TJ	BVDSS Temperature Coefficient	Reference to 25℃, I <sub>D</sub> =1mA		0.034		V/°C	
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =5A		18	26	mΩ	
KD3(ON)	Static Dialii-Source Off-Nesistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =4A		25.0	35	11122	
VGS(th)	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	1.6	2.5	V	
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	VGS-VDS , ID -2500A		-4.56		mV/℃	
IDSS	Drain-Source Leakage Current	$V_{DS}$ =32 $V$ , $V_{GS}$ =0 $V$ , $T_{J}$ =25 $^{\circ}$ C			1	uA	
1000	Drain-Source Leakage Guirent	$V_{DS}$ =32V , $V_{GS}$ =0V , $T_{J}$ =55 $^{\circ}$ C			5 uA	uд	
IGSS	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V	<b></b> '		±100	nA	
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =5A		14		S	
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		2.6		Ω	
Qg	Total Gate Charge (4.5V)	Q <sub>1</sub>		5.5			
Qgs	Gate-Source Charge	V <sub>DS</sub> =20V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A		1.25		nC	
Q <sub>gd</sub>	Gate-Drain Charge			2.5			
Td(on)	Turn-On Delay Time	G		8.9			
Tr	Rise Time	$V_{DD}$ =20V , $V_{GS}$ =10V , $R_{G}$ =3.3 $\Omega$		2.2		no	
Td(off)	Turn-Off Delay Time	I <sub>D</sub> =1A		41		ns	
T <sub>f</sub>	Fall Time	<b>\'</b>		2.7			
Ciss	Input Capacitance			593			
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		76		pF	
Crss	Reverse Transfer Capacitance			56			
Is	Continuous Source Current <sup>1,5</sup>	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\			6.1	Α	
ISM	Pulsed Source Current <sup>2,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			23	Α	
VSD	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃			1.2	V	

#### Note:

- 1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2. The data tested by pulsed , pulse width  $\leq 300 \text{us}$  , duty cycle  $\leq 2\%$
- 3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V, $V_{GS}$ =10V,L=0.1mH,I<sub>AS</sub>=20A
- 4. The power dissipation is limited by 150 ℃ junction temperature
- 5 . The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



## **40V N+P-Channel Enhancement Mode MOSFET**

### **Electrical Characteristics (Tc=25** ℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-40	-44		V
△BVDSS/△TJ	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25℃ , I <sub>D</sub> =-1mA		-0.02		V/°C
DDC(ON)	Static Ducin Source On Bosistance	V <sub>GS</sub> =-10V , I <sub>D</sub> =-6A	1	32	42	0
RDS(ON) Static Drain-Source On-Resistance <sup>2</sup>		V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-3A		48	60	mΩ
VGS(th)	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250 <b>u</b> A	-1.2	-1.7	-2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	VGS-VDS , ID250UA		3.72		mV/℃
IDCC	Duein Course Leekens Current	V <sub>DS</sub> =-32V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃	-		1	
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =-32V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C	74		5	uA
IGSS	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-6A		13		S
Qg	Total Gate Charge (-4.5V)	0.		11.5		
Qgs	Gate-Source Charge	V <sub>DS</sub> =-20V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-6A		3.5		nC
Q <sub>gd</sub>	Gate-Drain Charge			3.3		
Td(on)	Turn-On Delay Time	6		22		
Tr	Rise Time	$V_{DD}$ =-15V, $V_{GS}$ =-10V, $R_{G}$ =3.3 $\Omega$ ,		15.7		
Td(off)	Turn-Off Delay Time	I <sub>D</sub> =-1A		59		ns
T <sub>f</sub>	Fall Time			5.5		
Ciss	Input Capacitance			1415		
Coss	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz		134		pF
Crss	Reverse Transfer Capacitance			102		
ls	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-6	Α
ISM	Pulsed Source Current <sup>2,5</sup>				-22	Α
VSD	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25℃			-1.2	V

#### Note

- $1 \ensuremath{^{\circ}}$  The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2. The data tested by pulsed , pulse width  $\leq 300 us$  , duty cycle  $\leq 2\%$
- 3. The EAS data shows Max. rating . The test condition is VDD=-25V,VGS=-10V,L=0.1mH,IAS=-24A
- 4. The power dissipation is limited by 150°C junction temperature
- 5 . The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.





### **40V N+P-Channel Enhancement Mode MOSFET**

### N-Channel Typical Characteristics

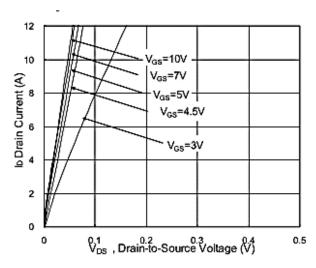


Fig.1 Typical Output Characteristics

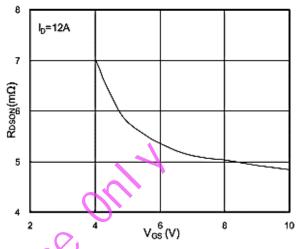


Fig.2 On-Resistance vs. G-S Voltage

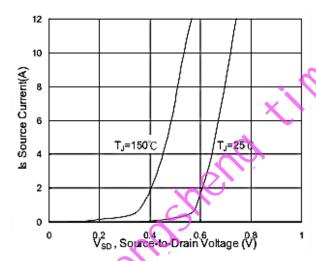


Fig.3 Forward Characteristics Of Reverse

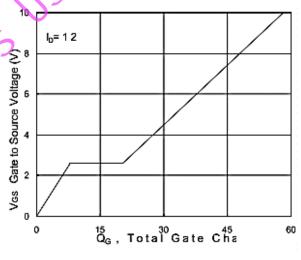


Fig.4 Gate-Charge Characteristics

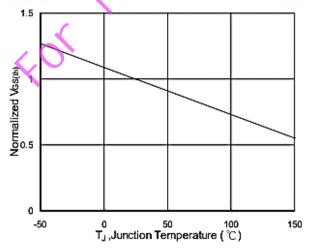


Fig.5 Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>

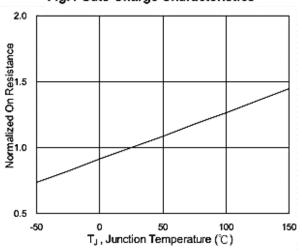


Fig.6 Normalized Roson vs. TJ



### **40V N+P-Channel Enhancement Mode MOSFET**

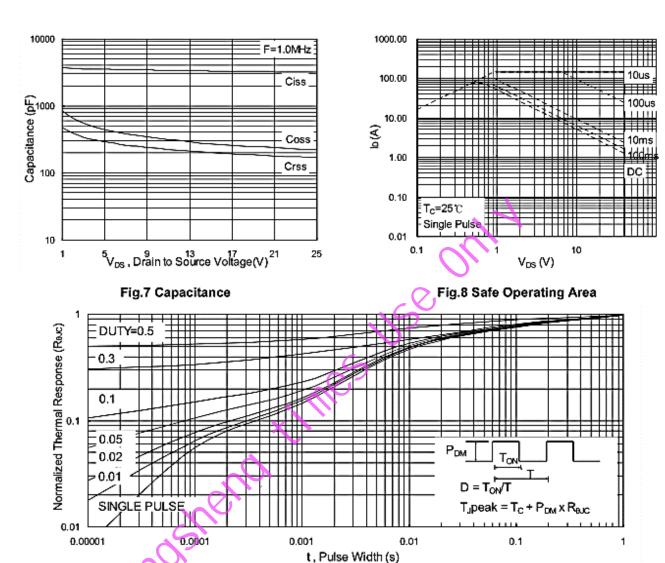


Fig.9 Normalized Maximum Transient Thermal Impedance

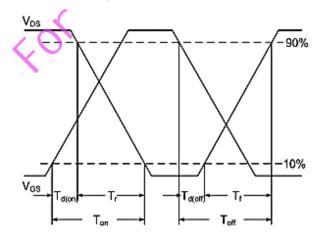


Fig.10 Switching Time Waveform

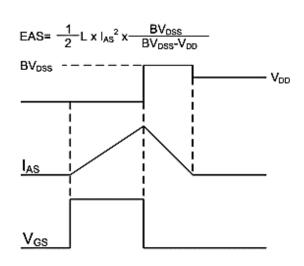


Fig.11 Unclamped Inductive Switching Wave





### **40V N+P-Channel Enhancement Mode MOSFET**

### P-Channel Typical Characteristics

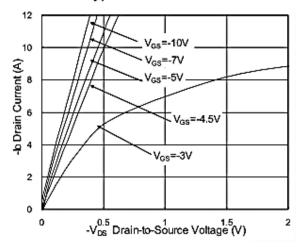


Fig.1 Typical Output Characteristics

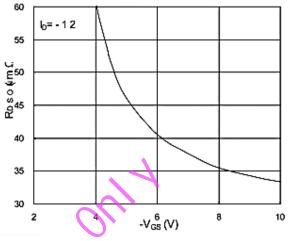


Fig.2 On-Resistance v.s Gate-Source

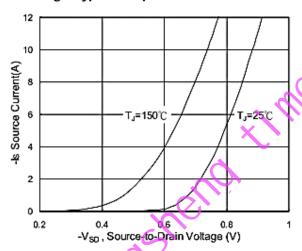


Fig.3 Forward Characteristics of Reverse

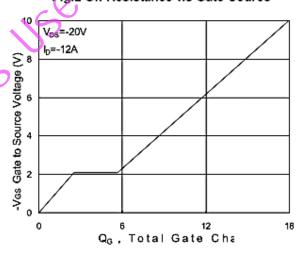


Fig.4 Gate-Charge Characteristics

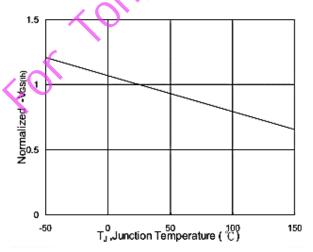


Fig.5 Normalized V<sub>GS(th)</sub> v.s T<sub>J</sub>

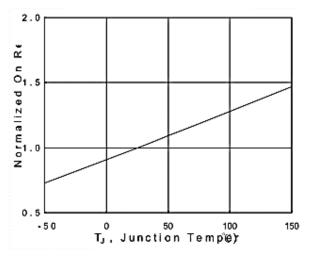
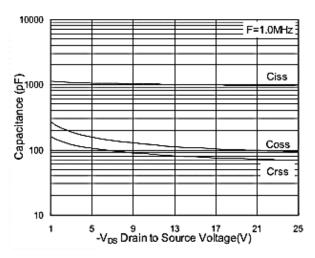


Fig.6 Normalized RDSON v.s TJ



### **40V N+P-Channel Enhancement Mode MOSFET**



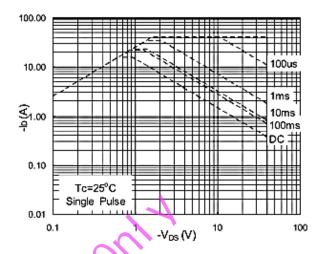


Fig.7 Capacitance

Fig.8 Safe Operating Area

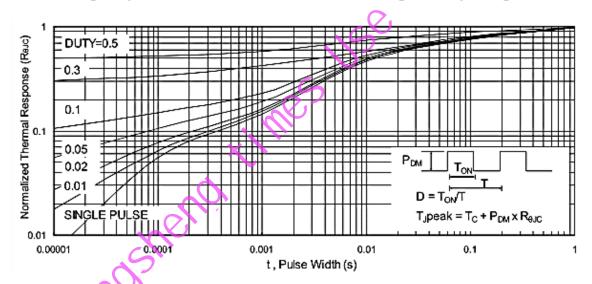


Fig.9 Normalized Maximum Transient Thermal Impedance

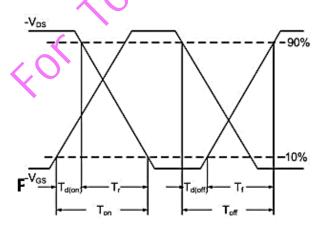


Fig.10 Switching Time Waveform

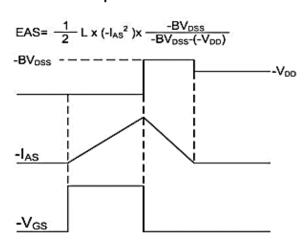
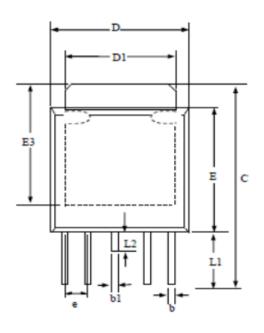


Fig.11 Unclamped Inductive Waveform



## **40V N+P-Channel Enhancement Mode MOSFET**

# Package Mechanical Data:TO-252-4L

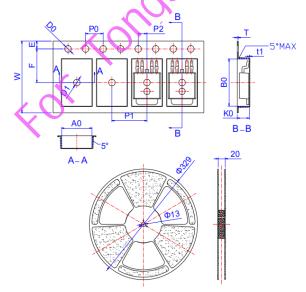


†	
Å ±	M
t	TO T
	L

SYMBOLS	Millimeters				
311.2023	MIN	NOM	MAX		
D	6.30	6.55	6.80		
D1	4.80	5.35	5.90		
С	9.30	9.75	10.20		
E	5.30	5.80	6.30		
E3	4.50	5.15	5.80		
L	0.90	1.35	1.80		
Ll	2.00	2,53	3.05		
L2	0.50	0.85	1.20		
ь _	0.30	0.50	0.70		
bl	0.40	0.60	0.80		
A	2.10	2.30	2.50		
A2	0.40	0.53	0.65		
A1	0.00	0.10	0.20		
e	1.20	1.30	1.40		

- All Dimensions Are in Millimeters
- Dimension Does Not Include Mold Protrusions.

# Reel Spectification-TO-252-4



	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
Е	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
В0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
Т	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583



### 40V N+P-Channel Enhancement Mode MOSFET

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### **40V N+P-Channel Enhancement Mode MOSFET**

Edition	Date	Change
RVE1.0	2018/1/31	Initial release

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