#### NCE N-Channel and P-Channel Enhancement Mode Power MOSFET

#### **Description**

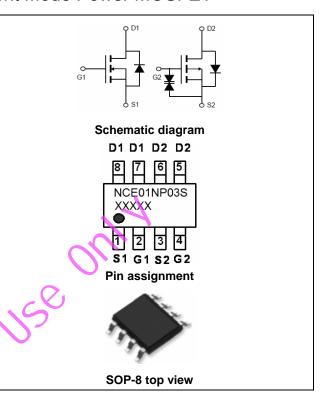
The NCE01NP03S uses advanced trench technology to provide excellent R<sub>DS(ON)</sub> and low gate charge. This device is suitable for use in inverter and other applications.

#### **Genera Features**

#### N-channel

#### P-channel

- $V_{DS} = 100V, I_{D} = 3A$
- $V_{DS} = -100V, I_{D} = -3A$
- $R_{DS(ON)}$  <130m $\Omega$  @  $V_{GS}$ =10V  $R_{DS(ON)}$  <200m $\Omega$  @  $V_{GS}$ =-10V
- $R_{DS(ON)}$  <140m $\Omega$  @  $V_{GS}$ =4.5V  $R_{DS(ON)}$  <230m $\Omega$  @  $V_{GS}$ =-4.5V
- High Power and current handing capability
- Lead free product is acquired



### Package Marking and Ordering Information

Device Marking	Device	De	evice Package	Reel Size	Tape width	Quantity
NCE01NP03S	NCE01NP03S	~	SOP-8	Ø330mm	12mm	4000 units

## Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Paramet	Parameter		N-channel	P-channel	Unit
Drain-Source Voltage		V <sub>DS</sub>	100	-100	V
Gate-Source Voltage		$V_{GS}$	±20	±20	V
Drain Current-Continuous (Note 2)	T <sub>A</sub> =25°C		3	-3	Α
Drain Current-Continuous	T <sub>A</sub> =70°C	I <sub>D</sub>	2.45	-2.45	۸
Drain Current -Pulsed (Note 1)		I <sub>DM</sub>	12	-12	Α
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	2	2	W
Operating Junction and Storage Te	emperature Range	$T_{J}, T_{STG}$	-55 To 150	-55 To 150	$^{\circ}$ C

#### **Thermal Characteristic**

Parameter	Symbol	Тур	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 2) (N-channel)	$R_{\theta JA}$	-	62.5	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2) (P-channel)	$R_{\theta JA}$	-	62.5	°C/W

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## N-channel Electrical Characteristics (T<sub>C</sub>=25 <sup>°</sup>C unless otherwise noted)

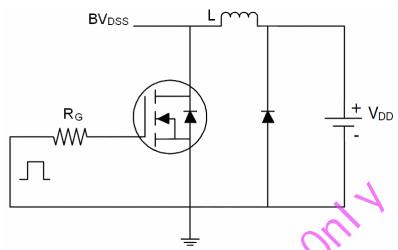
Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics			•				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	100	110	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V,V <sub>GS</sub> =0V	-	-	1	μA	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.0	1.5	2.0	V	
Dunin Course On Ctata Basistanas	0	V <sub>GS</sub> =10V, I <sub>D</sub> =3A	-	95	130	mΩ	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A		100	140	11177	
Forward Transconductance	<b>G</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =3A	3.5	-	-	S	
Dynamic Characteristics (Note4)		•	L.				
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =0V,	-	730	-	PF	
Output Capacitance	Coss	V <sub>DS</sub> =50V,V <sub>GS</sub> =0V, F=1.0MHz	-	37	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	r-1.UNITZ	-	27	-	PF	
Switching Characteristics (Note 4)		160					
Turn-on Delay Time	t <sub>d(on)</sub>		-	11	-	nS	
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =50V, $R_L$ =15 $\Omega$	-	7.4	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V,R <sub>G</sub> =2.5 $\Omega$	-	35	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	9.1	-	nS	
Total Gate Charge	$Q_g$	V -50VI -2A	-	21.5		nC	
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ =50V, $I_{D}$ =3A, $V_{GS}$ =10V	-	3.2	-	nC	
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> -10V	-	6	-	nC	
Drain-Source Diode Characteristics	<u> </u>						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}$ =0 $V$ , $I_{S}$ =3 $A$	-	-	1.2	V	
Diode Forward Current (Note 2)	Is		-	-	3	Α	
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF =3A	-	26		nS	
Reverse Recovery Charge	Qrr	di/dt = 100A/µs <sup>(Note3)</sup>	-	27		nC	
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD					

#### Notes:

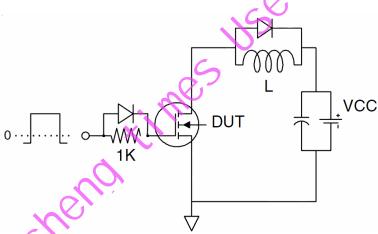
- 1. Repetitive Rating. Pulse width limited by maximum junction temperature.
- 2. The value of R  $_{BJA}$  is measured with the device mounted on 1in  $^2$  FR-4 board with 2oz. Copper, in a still air environment with T  $_A$ =25°C. The value in any given application depends on the user's specific board design. Surface Mounted on FR4 Board, t  $\leq$  10 sec. The current rating is based on the t  $\leq$  10s thermal resistance rating.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- $\textbf{4.} \ \textbf{Guaranteed by design, not subject to production} \ .$

### **Test Circuit**

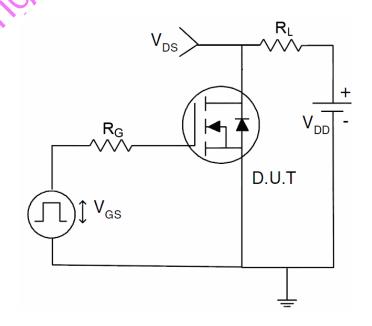
## 1) E<sub>AS</sub> test Circuit



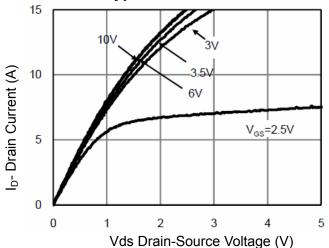
## 2) Gate charge test Circuit



## 3) Switch Time Test Circuit







**Figure 1 Output Characteristics** 

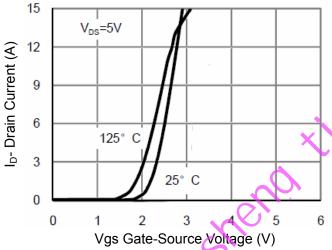


Figure 2 Transfer Characteristics

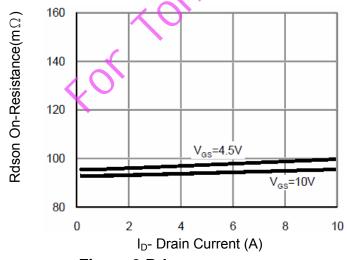


Figure 3 Rdson- Drain Current

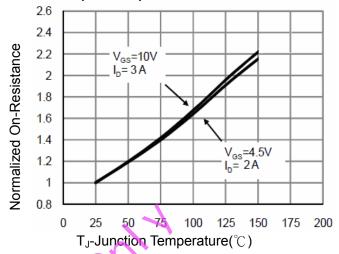


Figure 4 Roson-Junction Temperature

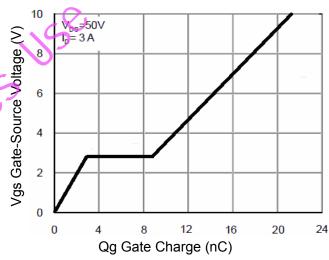


Figure 5 Gate Charge

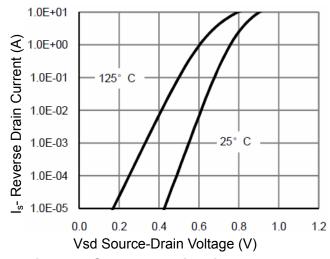
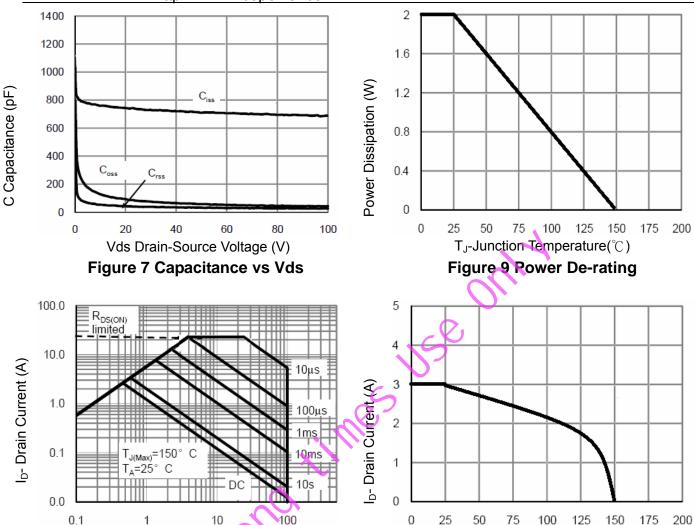


Figure 6 Source- Drain Diode Forward





Vds Drain-Source Voltage (V) Figure 8 Safe Operation Area

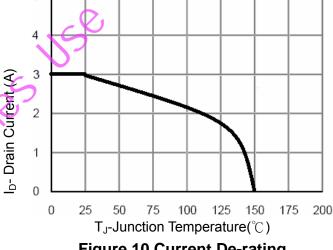


Figure 10 Current De-rating

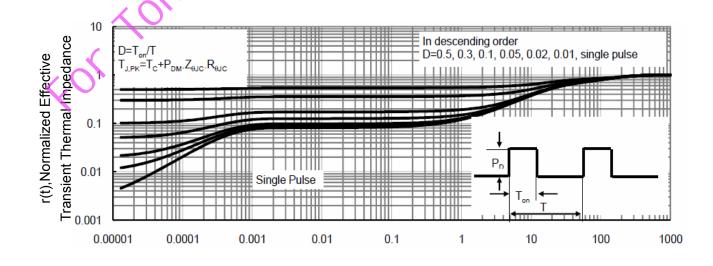


Figure 11 Normalized Maximum Transient Thermal Impedance

Square Wave Pluse Duration(sec)

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P-channel Electrical Characteristics (T<sub>A</sub>=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics			•				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-100	-	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-100V,V <sub>GS</sub> =0V	-	-	1	μA	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±10	μA	
On Characteristics (Note 3)							
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-1	-1.9	-3	V	
Drain-Source On-State Resistance	В	V <sub>GS</sub> =-10V, I <sub>D</sub> =-3A	-	170	200	mO.	
Diam-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A		200	230	mΩ	
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-5V,I <sub>D</sub> =-3A	2	-	-	S	
Dynamic Characteristics (Note4)		•	14				
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =-25V,V <sub>GS</sub> =0V,	-	760	-	PF	
Output Capacitance	Coss	V <sub>DS</sub> 25V,V <sub>GS</sub> -0V, F=1.0MHz	-	260	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	r-1.0WHZ	-	170	-	PF	
Switching Characteristics (Note 4)		160					
Turn-on Delay Time	t <sub>d(on)</sub>		-	14	-	nS	
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-50V, $I_{D}$ =-3A	-	18	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{DD}$ =-50V, $I_{D}$ =-3A $V_{GS}$ =-10V, $R_{GEN}$ =9 $\Omega$	-	50	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	18	-	nS	
Total Gate Charge	$Q_g$	V - 50VI - 2A	-	25	-	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =-50V, $I_{D}$ =-3A, $V_{GS}$ =-10V	-	5	-	nC	
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> 10V	-	7	-	nC	
Drain-Source Diode Characteristics	$\bigcirc$						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}$ =0 $V$ , $I_{S}$ =-3 $A$	-	-	-1.2	V	
Diode Forward Current (Note 2)	Is	-	-	-	-3	Α	
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> =-3A	-	35	-	nS	
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	46	-	nC	
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by				y LS+LD)	

#### Notes:

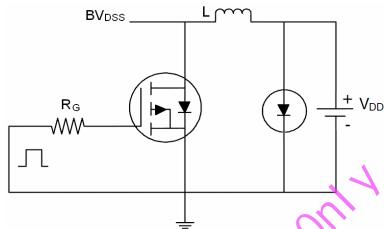
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- **5.** E<sub>AS</sub> condition: Tj=25  $^{\circ}$ C,V<sub>DD</sub>=-50V,V<sub>G</sub>=-10V,L=0.5mH,Rg=25 $\Omega$



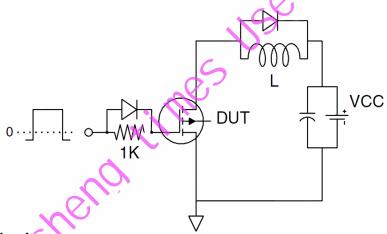


#### **Test Circuit**

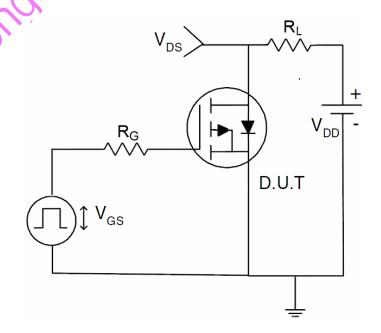
## 1) E<sub>AS</sub> Test Circuit



## 2) Gate Charge Test Circuit



## 3) Switch Time Test Circuit

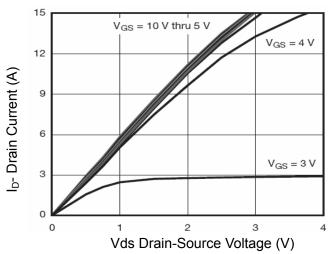


**Pb Free Product** 

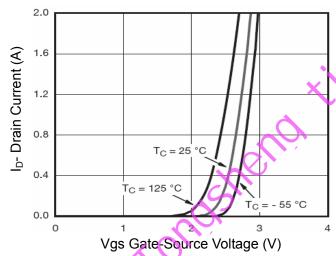


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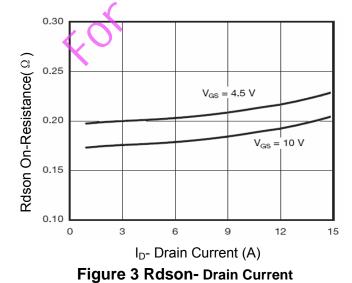
### **Typical Electrical and Thermal Characteristics (Curves)**



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 



Normalized On-Resistance 2.2 2.0  $I_{D} = 3 A$ V<sub>GS</sub> = 10V 1.8 1.6  $V_{GS} = 4.5 \text{ V}$ 1.4 1.2 1.0 0.8 0.6 - 50 100 125 150 ¬T」-Junction Temperature(°C)

Figure 4 Rdson-JunctionTemperature

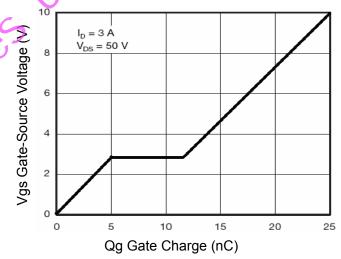


Figure 5 Gate Charge

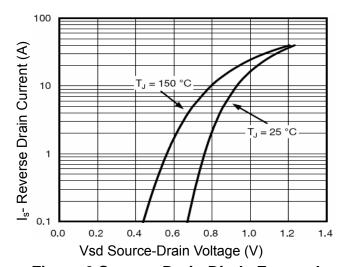
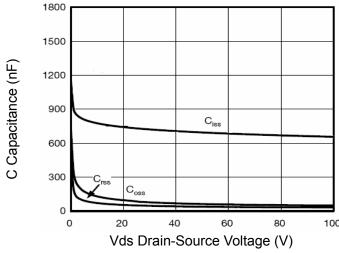


Figure 6 Source- Drain Diode Forward





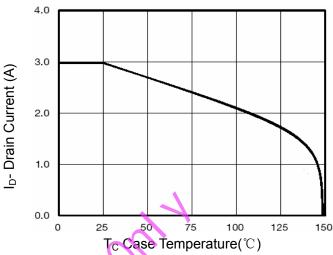
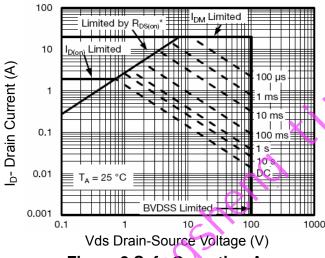


Figure 7 Capacitance vs Vds

Figure 9 Drain Current vs Case Temperature



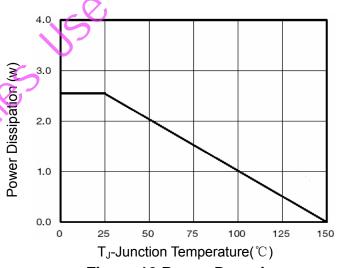
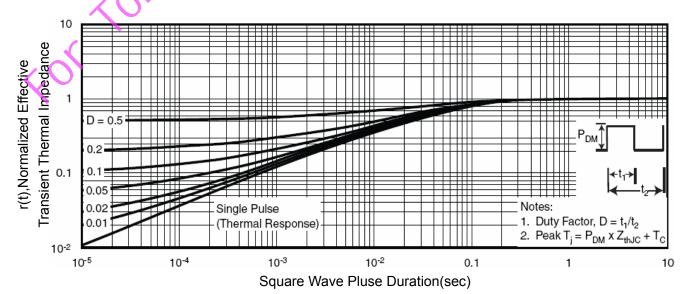


Figure 8 Safe Operation Area

Figure 10 Power De-rating

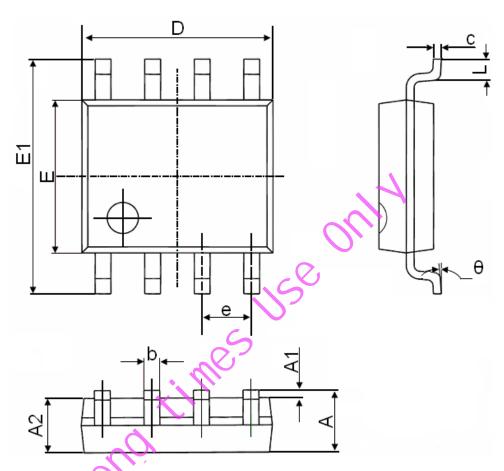


**Figure 11 Normalized Maximum Transient Thermal Impedance** 

**Pb Free Product** 



## **SOP-8 Package Information**



	( <u>/ / N</u>				
Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	1,350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	D(BSC)	0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	<b>0</b> °	8°	



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#### http://www.ncepower.com

## NCE01NP03S

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